

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"10813144".rlan. or ("10".src. and "813144".ap.)	US-PGPUB; USPAT; USOCR; DERWENT	OR	OFF	2007/02/22 10:01
S2	7	2003/0002596 2003/0002607 2003/0122588 2003/0123589 2003/0123594	US-PGPUB; USPAT	OR	OFF	2007/02/22 10:03
S4	66426	recover\$3 with (clock data)	US-PGPUB; USPAT	OR	OFF	2007/02/22 10:06
S3	4	S2 and recover\$3	US-PGPUB; USPAT	OR	OFF	2007/02/22 10:06
S7	4803	recover\$3 with (clock data) with loop with"PLL"	US-PGPUB; USPAT	OR	ON	2007/02/22 10:07
S6	4803	recover\$3 with (clock data) with loop	US-PGPUB; USPAT	OR	ON	2007/02/22 10:07
S5	66593	recover\$3 with (clock data)	US-PGPUB; USPAT	OR	ON	2007/02/22 10:07
S9	1095	recover\$3 with (clock data) with loop with PLL and (noise jitter\$4)	US-PGPUB; USPAT	OR	ON	2007/02/22 10:08
S8	1375	recover\$3 with (clock data) with loop with PLL	US-PGPUB; USPAT	OR	ON	2007/02/22 10:08
S11	407	recover\$3 with (clock data) with loop near3 PLL and (noise jitter\$4) with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:09
S10	430	recover\$3 with (clock data) with loop with PLL and (noise jitter\$4) with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:09
S12	14	recover\$3 with (clock data) with loop near3 PLL with (noise jitter\$4) with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:12
S15	16	recover\$3 with (clock data phase adj (shift offset)) near3 PLL with (noise jitter\$4) with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:13
S14	14	recover\$3 with (clock data phase adj (shift offset)) with loop near3 PLL with (noise jitter\$4) with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:13
S13	14	recover\$3 with (clock data pase adj (shift offset)) with loop near3 PLL with (noise jitter\$4) with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:13
S16	45	recover\$3 with (clock data phase adj (shift offset)) with PLL with (noise jitter\$4) with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:16

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S17	1349	recover\$3 near3 (clock data) with (phase adj (shift offset) noise jitter\$4) with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:17
S19	117	recover\$3 near2 (clock data) and (inject\$3 insert\$3) near3 (phase adj (shift offset) noise jitter\$4) with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:18
S20	52	recover\$3 near2 (clock data) and (inject\$3 insert\$3) adj (shift offset noise jitter\$4) with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:19
S21	4	recover\$3 near2 (clock data) same (inject\$3 insert\$3) adj (shift offset noise jitter\$4) with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:20
S22	3	recover\$3 near2 (clock data) same (inject\$3 insert\$3) adj (shift offset noise jitter\$4) with receiv\$3 and @ad<="20040330"	US-PGPUB; USPAT	OR	ON	2007/02/22 10:21
S23	38	recover\$3 near2 (clock data) and (inject\$3 insert\$3) adj (phase adj shift offset noise jitter\$4) with receiv\$3 and @ad<="20040330"	US-PGPUB; USPAT	OR	ON	2007/02/22 10:23
S25	34	recover\$3 near2 (clock data) and (inject\$3 insert\$3) adj (phase adj shift noise jitter\$4) with receiv\$3 NOT INTEL ADJ CORPORATION.as. and @ad<="20040330"	US-PGPUB; USPAT	OR	ON	2007/02/22 10:30
S29	2	((("5,835,501") or ("5,793,822")). PN.	USPAT	OR	OFF	2007/02/22 10:37
S27	31	"5,835,501" "5,793,822"	US-PGPUB; USPAT	OR	ON	2007/02/22 10:37
S26	15	recover\$3 near2 (clock data) and (inject\$3 insert\$3) adj (phase adj shift noise jitter\$4) with receiv\$3 and test\$3 NOT INTEL ADJ CORPORATION.as. and @ad<="20040330"	US-PGPUB; USPAT	OR	ON	2007/02/22 10:38
S32	622528	S27 and jitter\$4 and recover\$3 wiyh loop	US-PGPUB; USPAT	OR	ON	2007/02/22 10:43
S31	21	S27 and jitter\$4 and recover\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:43
S30	26	S27 and jitter\$4	US-PGPUB; USPAT	OR	ON	2007/02/22 10:43
S33	9	S27 and jitter\$4 and recover\$3 with loop	US-PGPUB; USPAT	OR	ON	2007/02/22 10:48
S37	26	(jitter\$4 noise) near3 receiver with recover\$3 with loop	US-PGPUB; USPAT	OR	ON	2007/02/22 10:50

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S36	171	(jitter\$4 noise) with recover\$3 with loop with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:50
S35	1794	(jitter\$4 noise) and recover\$3 with loop with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:50
S34	2	S27 and jitter\$4 and recover\$3 with loop with receiv\$3	US-PGPUB; USPAT	OR	ON	2007/02/22 10:50
S24	37	recover\$3 near2 (clock data) and (inject\$3 insert\$3) adj (phase adj shift offset noise jitter\$4) with receiv\$3 NOT INTEL ADJ CORPORATION.as. and @ad<="20040330"	US-PGPUB; USPAT	OR	ON	2007/02/22 12:08
S38	350	recover\$3 near2 (clock data) with (distor\$4 skew\$3) and (phase adj shift offset noise jitter\$4) with receiv\$3 NOT INTEL ADJ CORPORATION.as. and @ad<="20040330"	US-PGPUB; USPAT	OR	ON	2007/02/22 12:09
S40	33	recover\$3 near2 (clock data) with (distor\$4 skew\$3) with (phase adj (shift offset) noise jitter\$4) with receiv\$3 NOT INTEL ADJ CORPORATION.as. and @ad<="20040330"	US-PGPUB; USPAT	OR	ON	2007/02/22 12:12
S39	35	recover\$3 near2 (clock data) with (distor\$4 skew\$3) with (phase adj shift offset noise jitter\$4) with receiv\$3 NOT INTEL ADJ CORPORATION.as. and @ad<="20040330"	US-PGPUB; USPAT	OR	ON	2007/02/22 12:12